AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111
Serial Number: 10/603,374
Filing Date: June 25, 2003
Title: ON-DIE WAVEFORM CAPTURE
Assignee: Intel Corporation

Page 2
Dkt: 80107.016US1

IN THE CLAIMS

Please amend the claims as follows:

1.   (Withdrawn) A simultaneous bidirectional port circuit comprising:

a sampling circuit configured to sample an incoming waveform;

a receiver coupled to the sampling circuit configured to measure an amplitude of the incoming waveform;

a storage mechanism to store information from the receiver; and

a control mechanism configured to control the receiver and the sampling circuit to measure the amplitude of a repetitive incoming waveform at a plurality of time points.

2.   (Withdrawn) The simultaneous bidirectional port circuit of claim 1 wherein the receiver comprises a variable offset comparator.

3.   (Withdrawn) The simultaneous bidirectional port circuit of claim 1 wherein the storage mechanism comprises a counter.

4.   (Withdrawn) The simultaneous bidirectional port circuit of claim 1 wherein the storage mechanism comprises a shift register.

5.   (Withdrawn) The simultaneous bidirectional port circuit of claim 1 further comprising an output driver having an output node coupled to an input node of the sampling circuit.

6.   (Withdrawn) The simultaneous bidirectional port circuit of claim 1 wherein:

the receiver comprises a comparator; and

the simultaneous bidirectional port circuit further comprises a variable reference coupled to the comparator.

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111   Page 3
Serial Number: 10/603,374   Dkt: 80107.016US1
Filing Date: June 25, 2003
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7.   (Withdrawn) The simultaneous bidirectional port circuit of claim 1 wherein the control mechanism is configured to calculate a distribution for each of the plurality of time points.

8.   (Original) An integrated circuit comprising:

a signal node to receive a data signal; and

a port circuit coupled to the signal node, the port circuit configured to receive digital data from the signal node during a first mode of operation, and configured to capture a waveform of a signal on the signal node during a second mode of operation.

9.   (Original) The integrated circuit of claim 8 wherein the port circuit comprises a variable offset comparator having an input node coupled to the signal node.

10.   (Original) The integrated circuit of claim 8 wherein the port circuit comprises an output driver having an output coupled to the signal node.

11.   (Original) The integrated circuit of claim 10 wherein the port circuit is configured as a simultaneous bidirectional port circuit.

12.   (Original) The integrated circuit of claim 8 further comprising a clock input node to receive a clock signal.

13.   (Original) The integrated circuit of claim 12 wherein the port circuit further comprises a sampling circuit coupled to the clock input node to sample the signal on the signal node at various time points.

14.   (Original) The integrated circuit of claim 13 further comprising a storage mechanism to store information describing the waveform of the signal.

15.   (Original) The integrated circuit of claim 14 wherein the storage mechanism comprises a counter.

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111
Serial Number: 10/603,374
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Page 4
Dkt: 80107.016US1

16.   (Original) An electronic system comprising:

an integrated circuit including a signal node to receive a signal, and a port circuit coupled to the signal node, the port circuit configured to receive digital data from the signal node during a first mode of operation, and configured to capture a waveform of the signal on the signal node during a second mode of operation; and

a network interface capable of coupling the integrated circuit to a network.

17.   (Original) The electronic system of claim 16 wherein the port circuit comprises a variable offset comparator having an input node coupled to the signal node.

18.   (Original) The electronic system of claim 16 wherein the port circuit comprises an output driver having an output coupled to the signal node.

19.   (Original) The electronic system of claim 16 wherein the port circuit comprises a sampling circuit to sample the signal on the signal node at various time points.

20.   (Original) A method of capturing a waveform on an integrated circuit die comprising:
        sampling a simultaneous bidirectional data signal at a first time point;
        receiving the simultaneous bidirectional data signal at a receiver; and
        varying a threshold of the receiver.

21.   (Withdrawn) The method of claim 20 wherein sampling comprises subtracting an outgoing signal from an incoming signal.

22.   (Withdrawn) The method of claim 20 wherein receiving comprises receiving the simultaneous bidirectional data signal at a variable offset comparator.

23.   (Withdrawn) The method of claim 22 wherein varying a threshold comprises varying an offset of the variable offset comparator.

24.   (Withdrawn) The method of claim 23 further comprising:

sampling the simultaneous bidirectional data signal at a plurality of time points; and

varying the offset of the variable offset comparator at each of the plurality of time points.

25.   (Withdrawn) The method of claim 20 wherein the simultaneous bidirectional data signal
is repetitive, and sampling at a first time point comprises taking a plurality of samples at
substantially the same time with respect to the repetitive signal.

26.   (Withdrawn) The method of claim 25 further comprising varying the threshold during
the plurality of samples.

27.   (Withdrawn) A method comprising:

receiving a signal at a receiver configured to receive digital data and configured to
capture a waveform of the signal;

sampling the signal at a plurality of time points; and

varying a threshold of the receiver at each of the plurality of time points.

28.   (Withdrawn) The method of claim 27 wherein the signal is repetitive, and wherein
sampling comprises sampling the repetitive signal more than once at each of the plurality of time
points.

29.   (Withdrawn) The method of claim 28 wherein:

receiving comprises receiving the signal at a variable offset comparator; and

varying a threshold of the receiver comprises varying an offset of the variable offset
comparator.

30.   (Withdrawn) The method of claim 28 wherein sampling the repetitive signal more than
once at each of the plurality of time points provides a distribution, the method further comprising
creating a probability density function from the distribution.